



DT11 R PCT/PTO JUN 27 2002

10/049270

PATENT APPLICATION

IN THE UNITED STATES PATENT AND TRADEMARK OFFICE

In re application of

Docket No: Q68281

Hui ZHONG, et al.

Appln. No.: 10/049,270

Group Art Unit: Not yet designated

Confirmation No.: 9445

Examiner: Not yet designated

Filed: February 11, 2002

June 27, 2002

For: MULTILAYERED PRINTED CIRCUIT BOARD, SOLDER RESIST COMPOSITION,
MULTILAYERED PRINTED CIRCUIT BOARD MANUFACTURING METHOD, AND
SEMICONDUCTOR DEVICE

PETITION FOR EXTENSION OF TIME UNDER 37 C.F.R. § 1.136

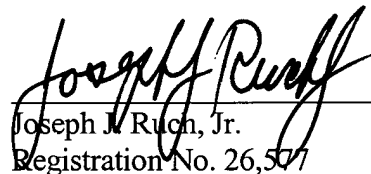
Commissioner for Patents
Washington, D.C. 20231

Sir:

Pursuant to 37 C.F.R. § 1.136, Applicant hereby petitions for an extension of time of one month, extending the time for responding to the Office Action of April 16, 2002 to July 16, 2002.

A check for the statutory fee of \$110.00 is attached. The USPTO is directed and authorized to charge all required fees, except for the Issue Fee and the Publication Fee, to Deposit Account No. 19-4880. Please also credit any overpayments to said Deposit Account. A duplicate copy of this sheet is enclosed.

Respectfully submitted,


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